

Figure 1b

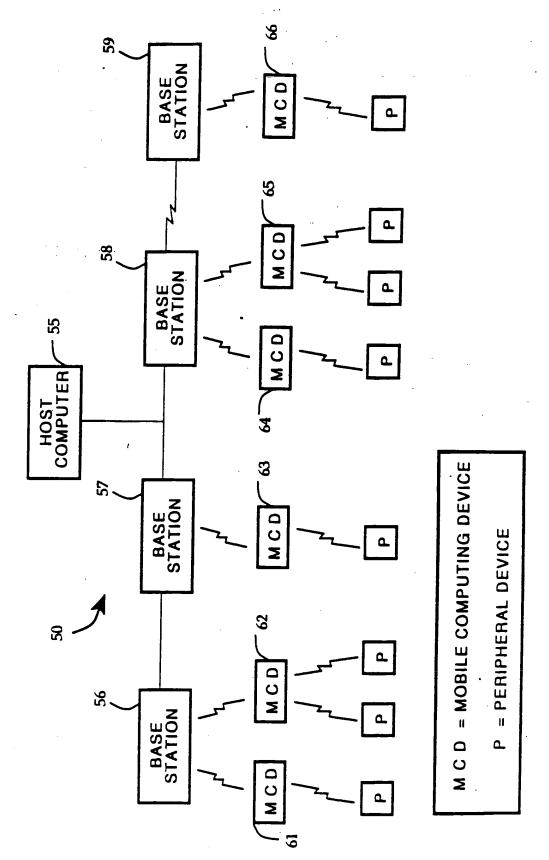


Figure 1c

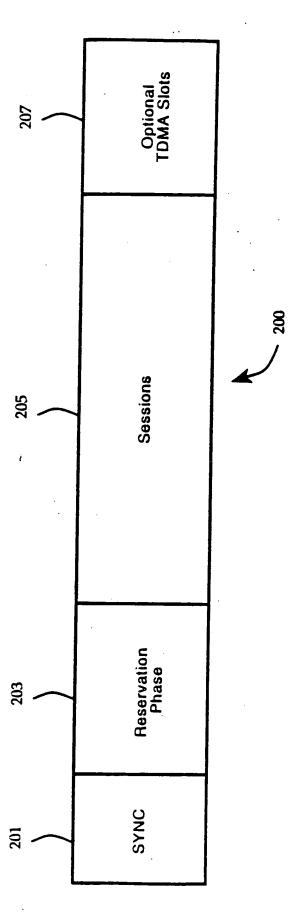


Figure 2

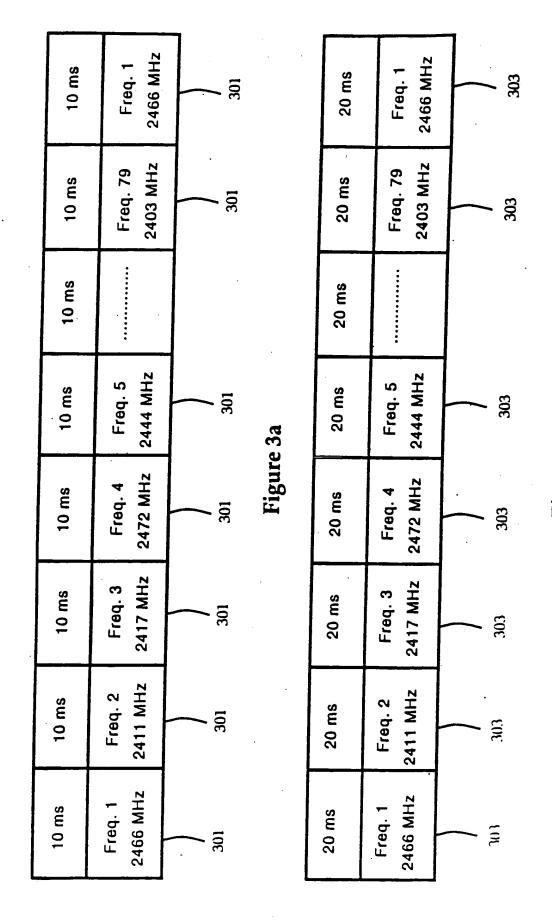


Figure 3b

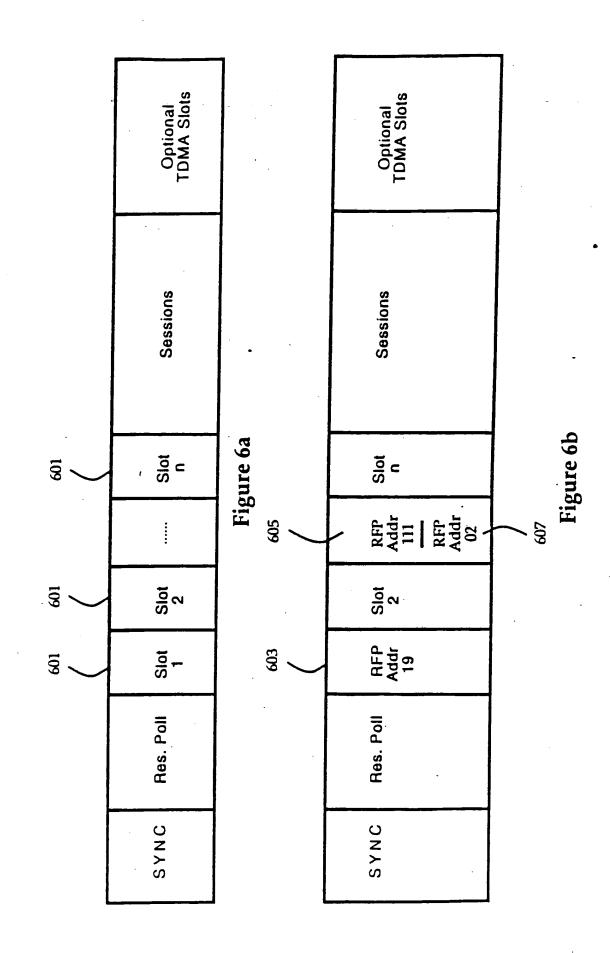
1164 1. 2400 MINZ		Freq 2: 2	2: 2411 MHz	Freq 3: 2417 MHz	417 MHz	Freq 5:	Freq 5: 2444 MHz
AI. 1	AI. 2	Al. 1	AI 2	-	Ţ		
			1 ::	-	۸۱. ۲	A. 1	AI. 2
zu ilis	z0 ms	20 ms	20 ms	20 ms	20 ms	20 ms	20 WC
							20113

Figure 4a

	
	AI. 4 20 ms
Freq 2: 2411 MHz	AI. 3 20 ms
Freq 2:	Al. 2 20 ms
	AI. 1 20 ms
	Al. 4 20 ms
3 MHz	AI. 3 20 ms
Freq 1: 2466 MHz	AI. 2 20 ms
1	Al. 1 20 ms

Figure 4b

Figure 5b



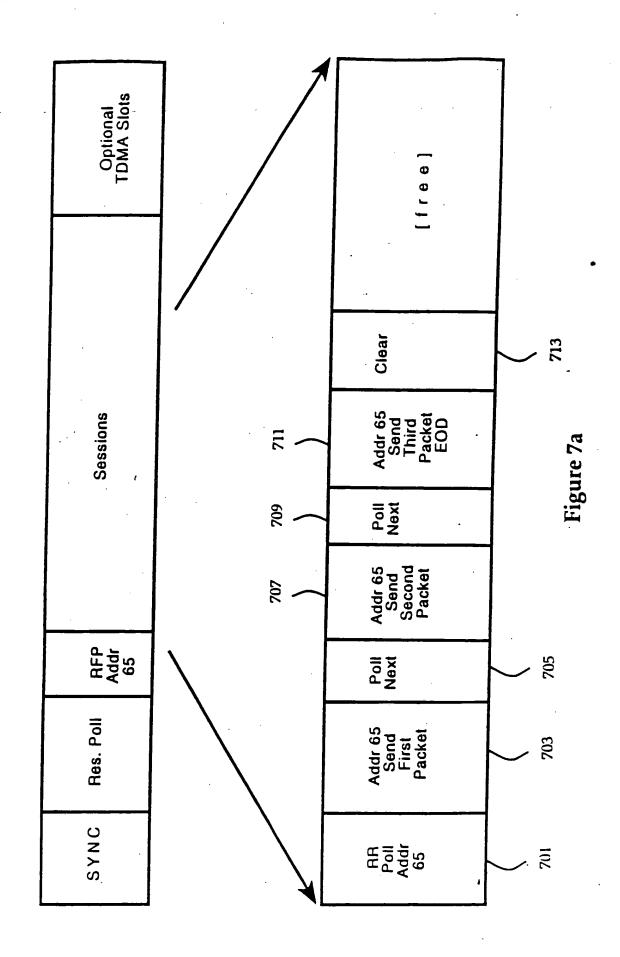
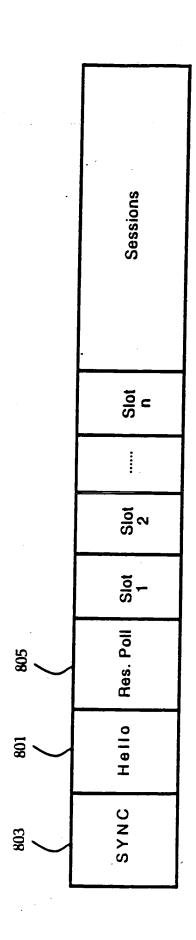
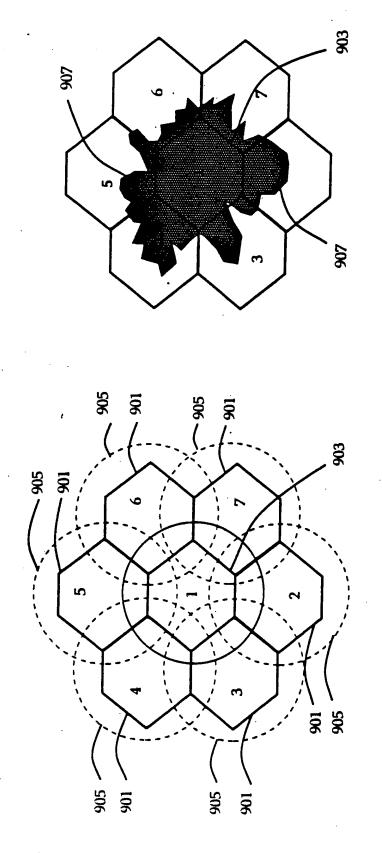
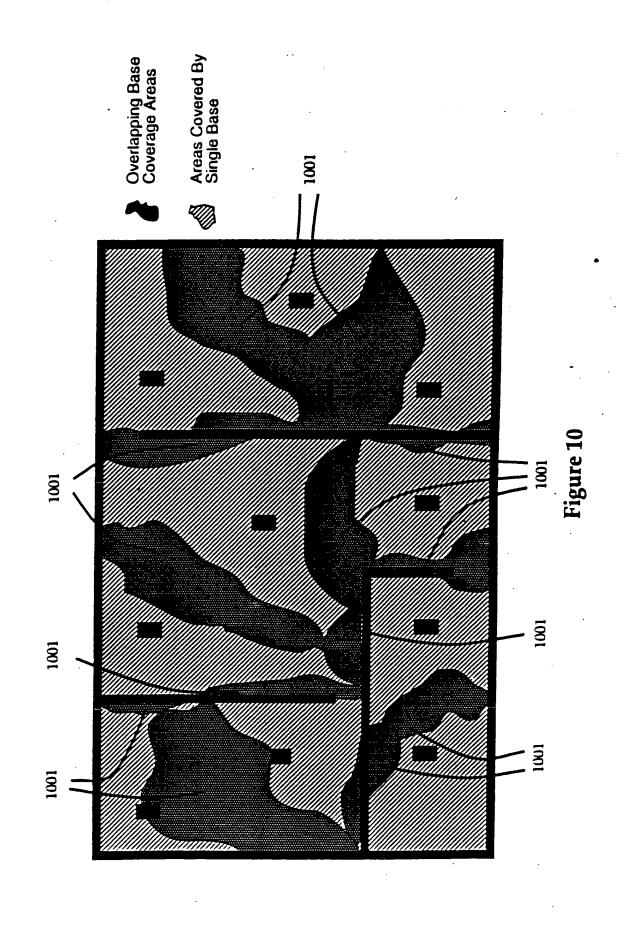


Figure 7b







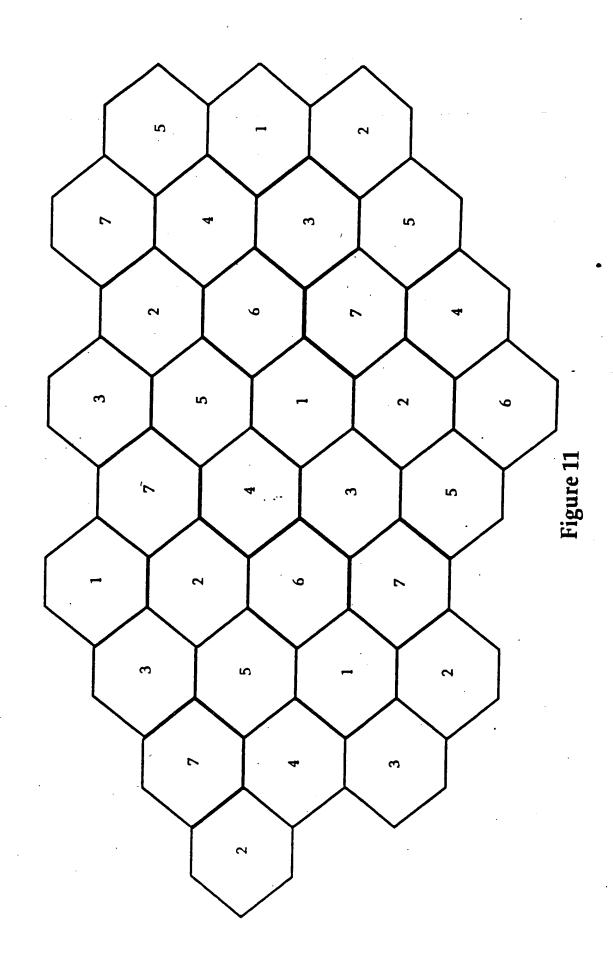
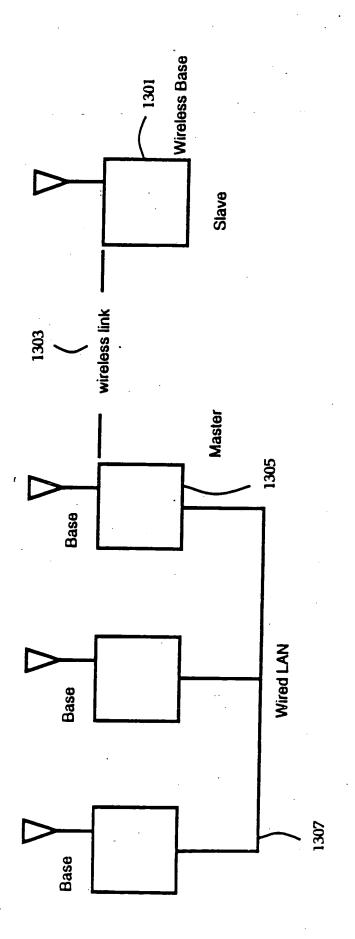
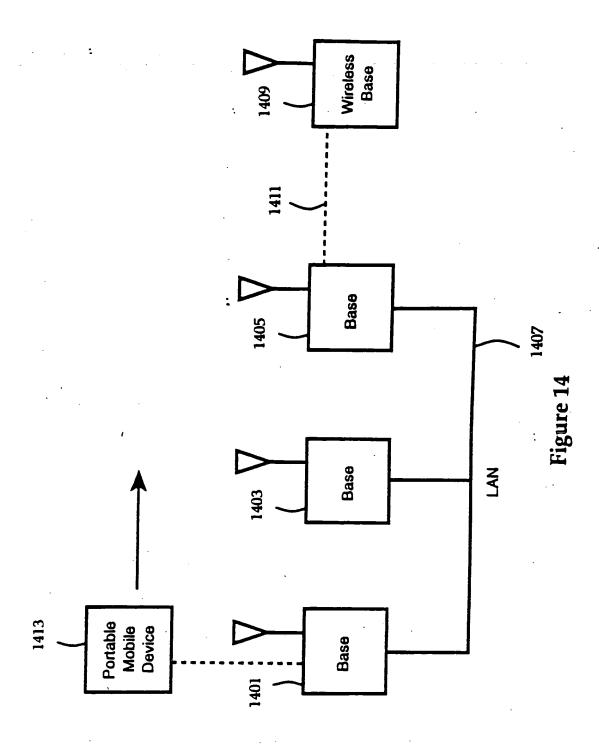


Figure 12





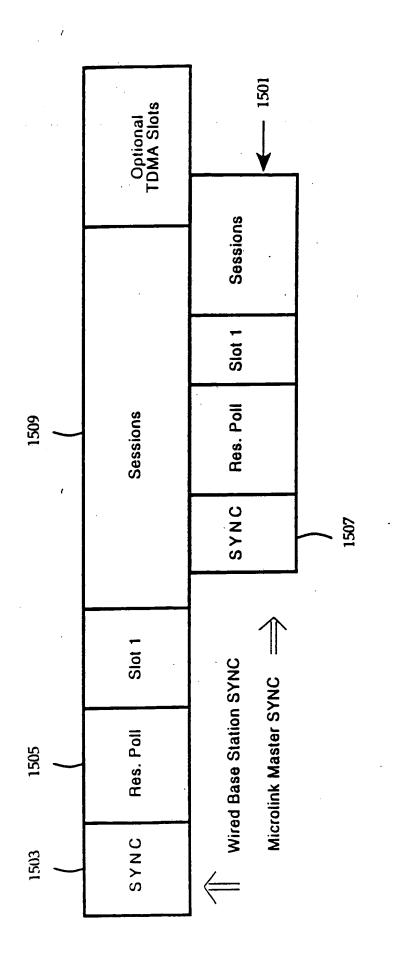


Figure 15

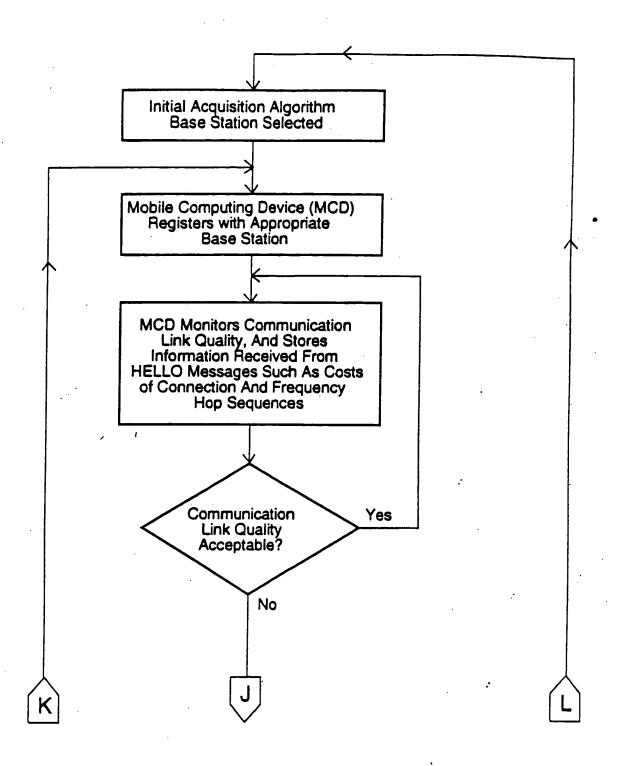
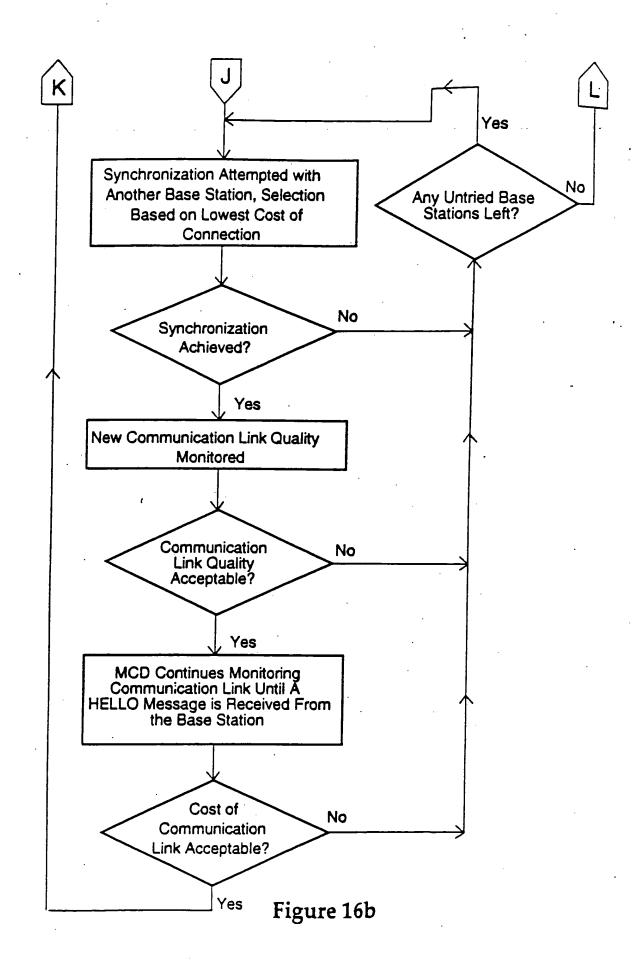


Figure 16a



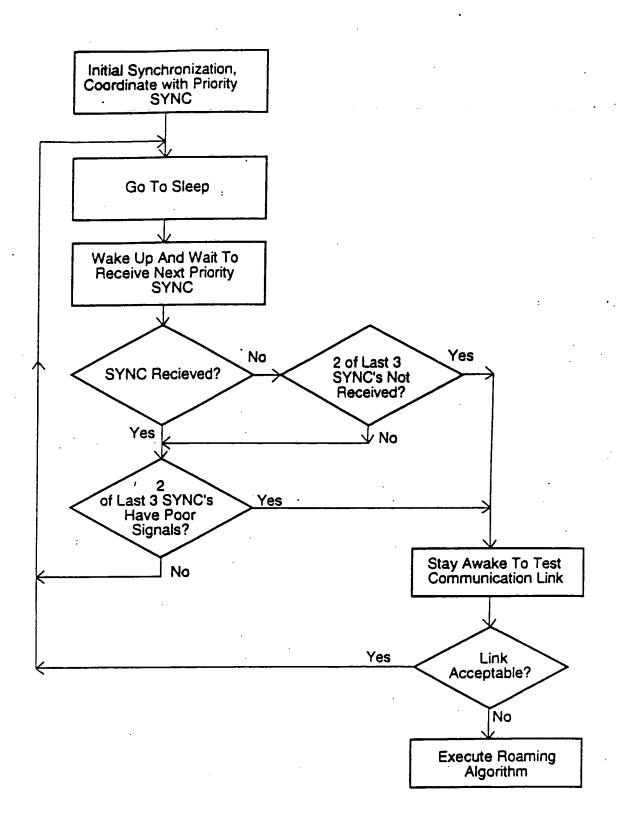
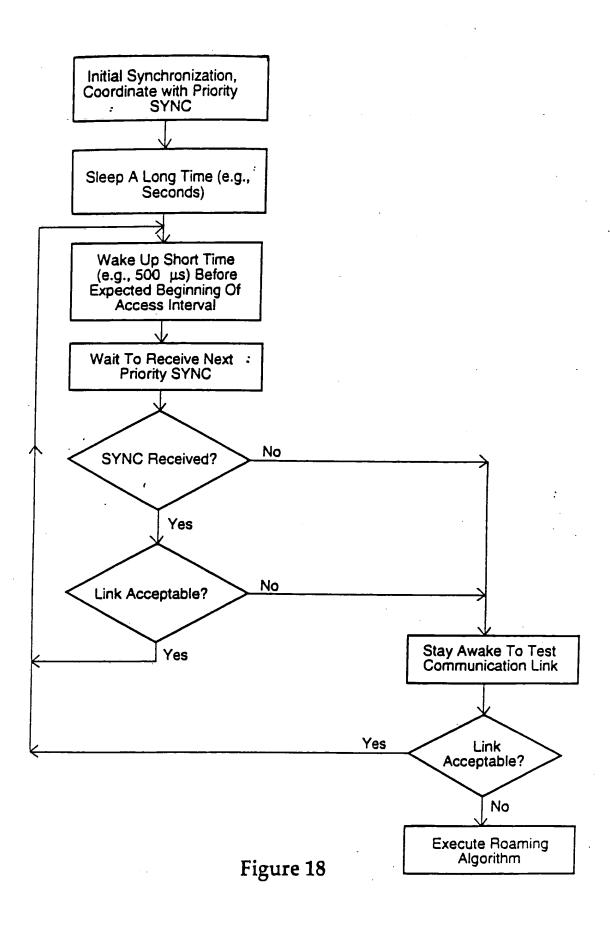
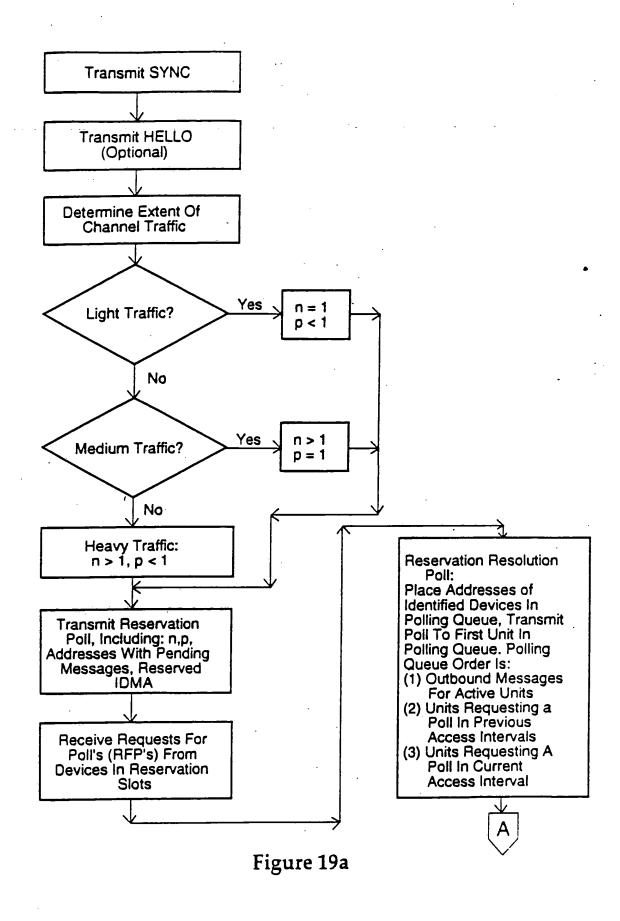


Figure 17





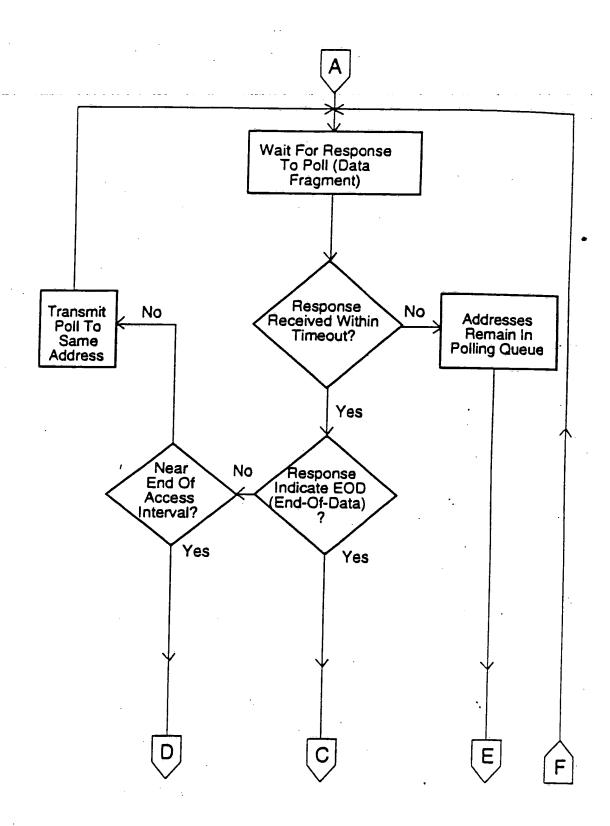


Figure 19b-1

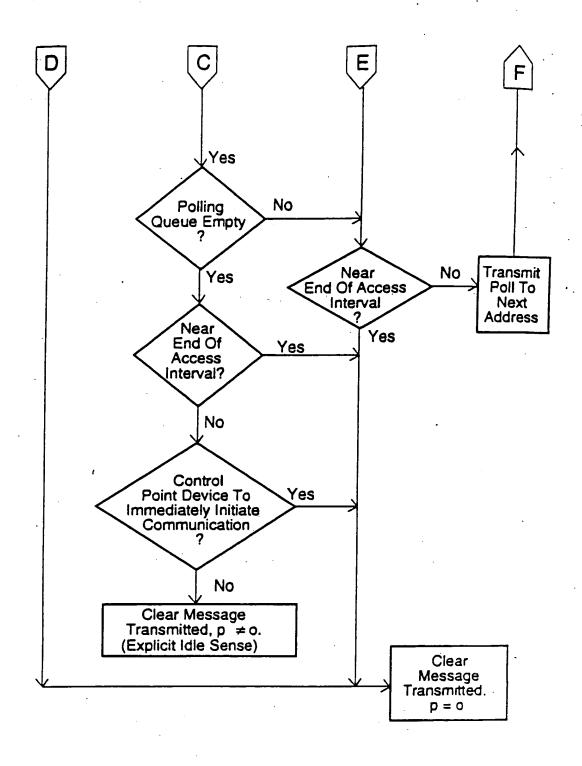


Figure 19b-2

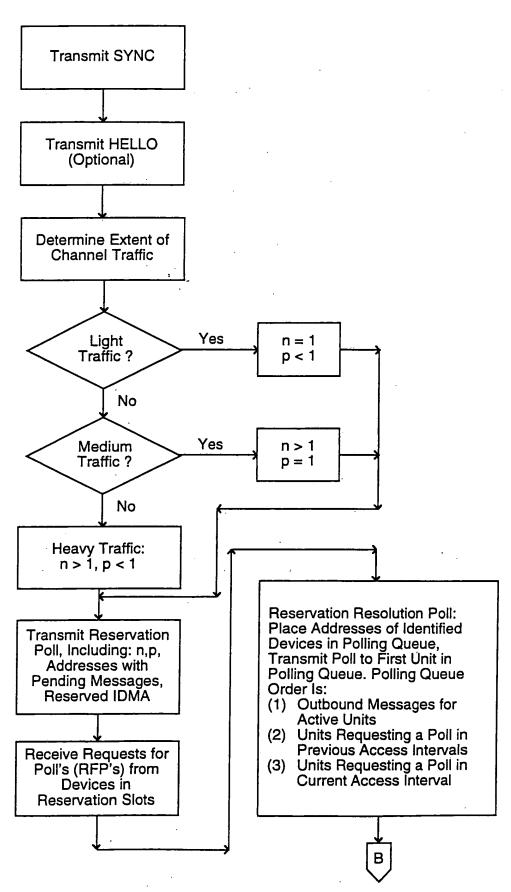


Fig. 20a

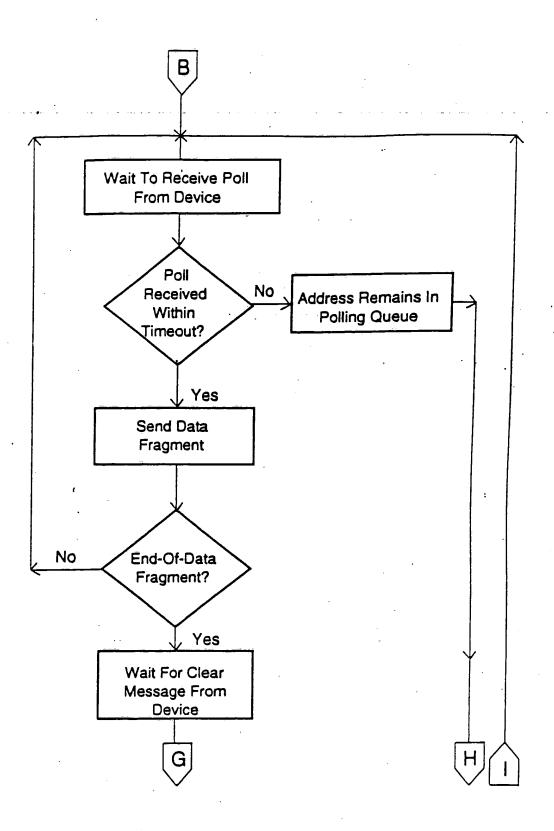


Figure 20b-1

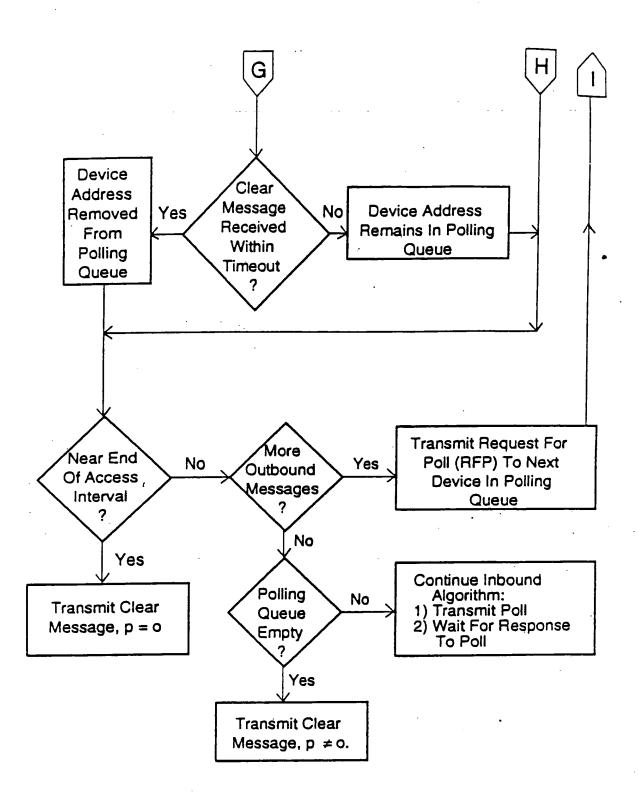
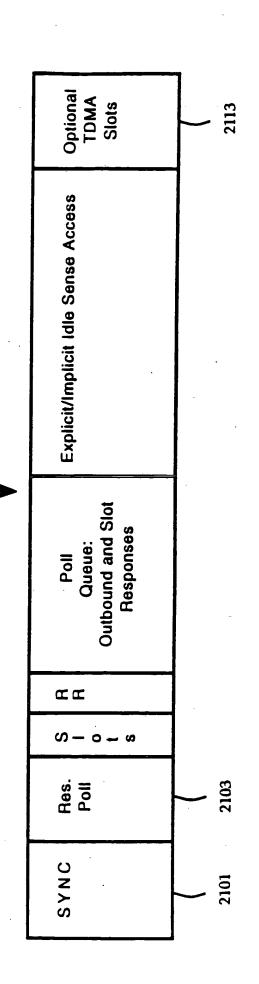


Figure 20b-2



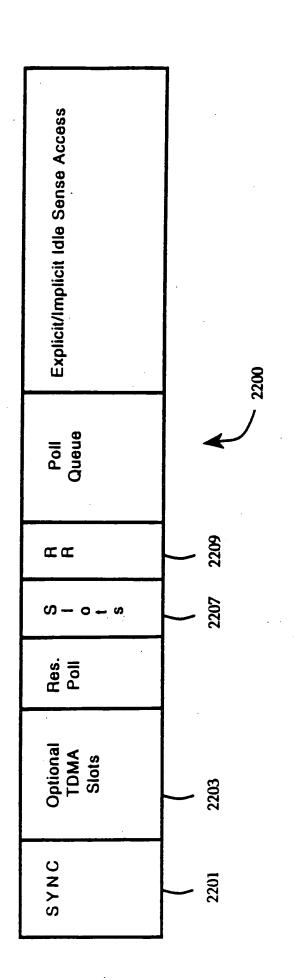


Figure 22

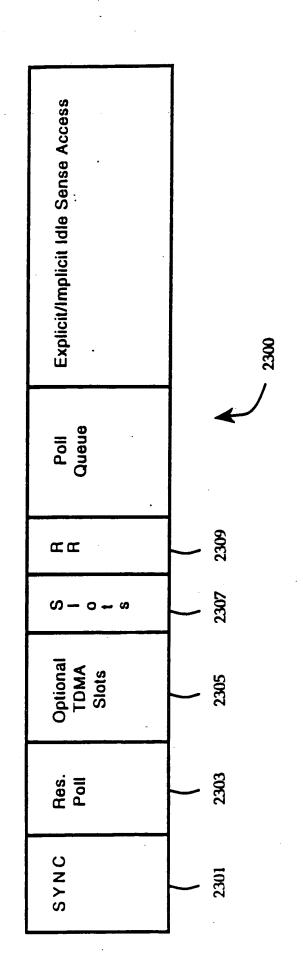


Figure 23

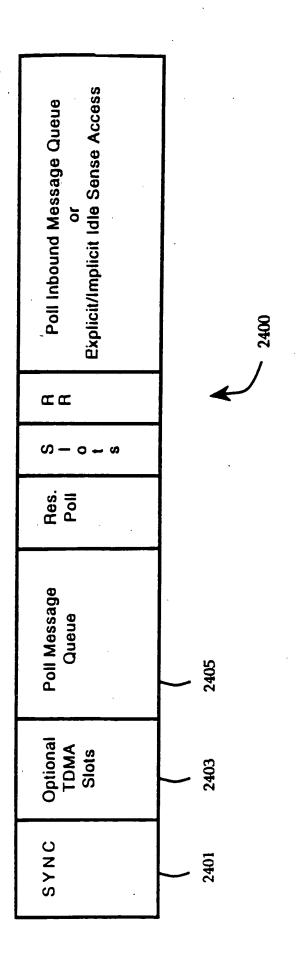
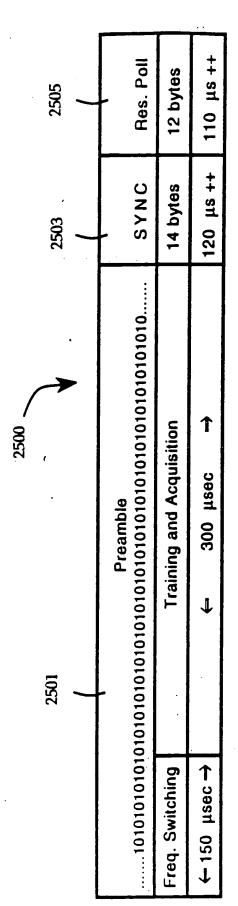


Figure 24



++ allows additional bit times for HDLC zero bit insertion

0
ą
2
. 4

		SYNC \$\int 2600
Вуте	Content	Definition
_	FLAG	Frame Delimiter
2	Dest. Address 1	Destination Address (SYNC = Broadcast = FFFF _H)
3	Dest. Address 2	Destination Address, 2nd Byte
4	TYPE	Message Type=SYNC, Priority Sync, or Priority SYNC with
5	Source Address 1	Control Point Address
9	Source Address 2	Control Point Address, 2nd Byte
7*	SYSTEM ID	System Identifier (LAN ID)
8*	NET ID	OI 19N
•6	Command/Status 1	Control Point Status
		2 Bits Control Point Type
		2 Bits Loading Factor
		1 Bit Scheduled Service Availability
	·	3 Bits Reserved
<u>1</u> 0.	Command/Status 2	Scheduled Services
		5 Bits Number of Allocated TDMA Time Slots
		3 Bits Number of Available Time Slots
11.	Random	Randomly Selected Character for Improved Engaging
12	CRC 1	Error Check
13	CRC 2	Error Check
14	FLAG	Frame Delimiter

Figure 26

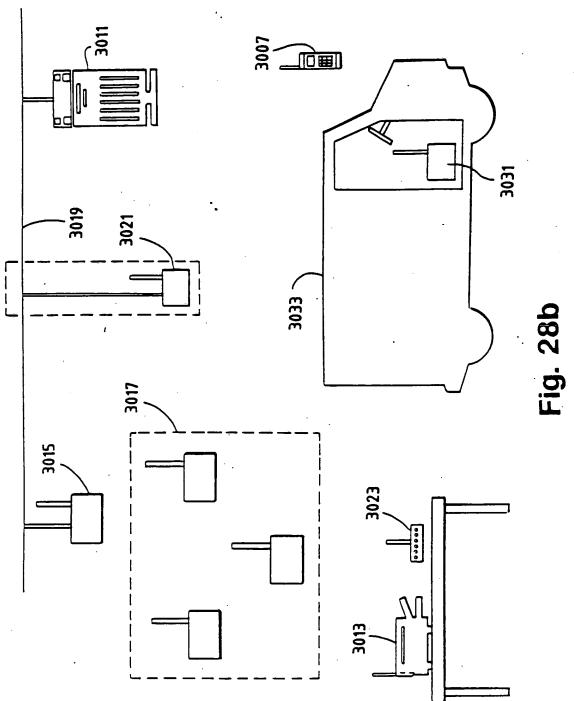
*Optionally Encrypted Byt s

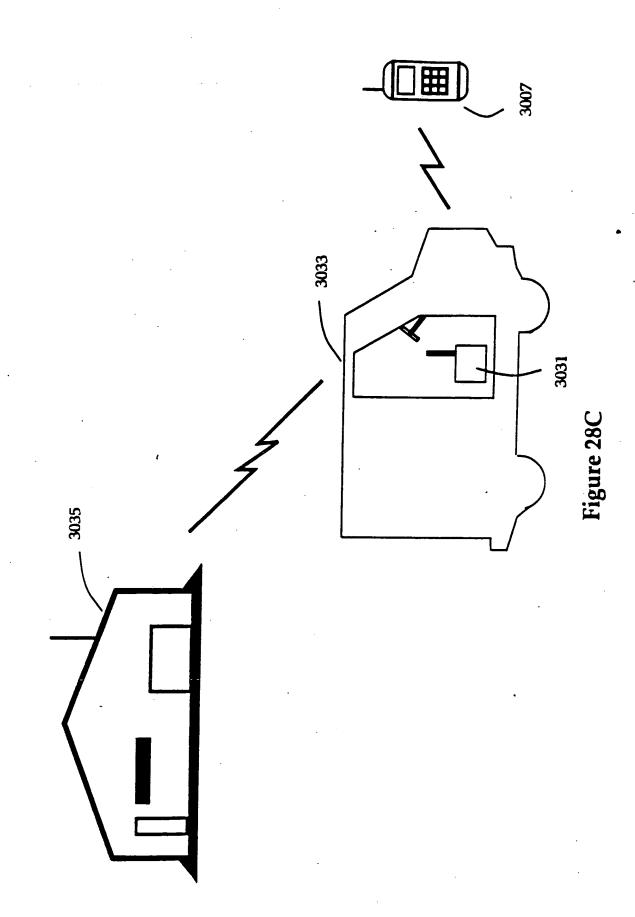
Reservation Poll

Definition	Frame Delimiter	Destination Address (RES = Broadcast = FFFF _H)	Destination Address, 2nd Byte	Message Type = RES POLL	3 Bits Number of Slots	5 Bits Probability Factor	Hopping Sequence ID	Hopping Sequence Index		3 Bits Hop Rate	2 Bits Al's per Hop	3 Bits Reserved	Error Check	Error Check	Frame Delimiter
Content	FLAG	Dest. Address 1	Dest. Address 2	TYPE	SLOT PARMS		Sequence	Index	NET PARMS				CRC 1	CRC 2	FLAG
Вуте	1	2	3	4	5	9	7*	8*	* 6				10	11	12

Figure 27

Fig. 28A





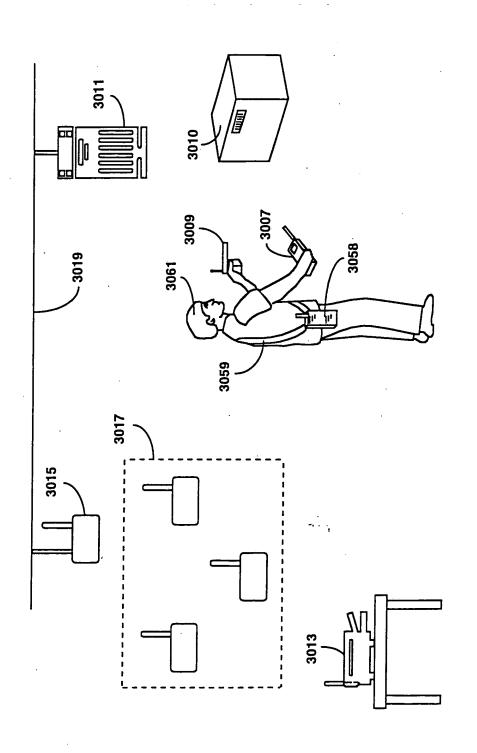
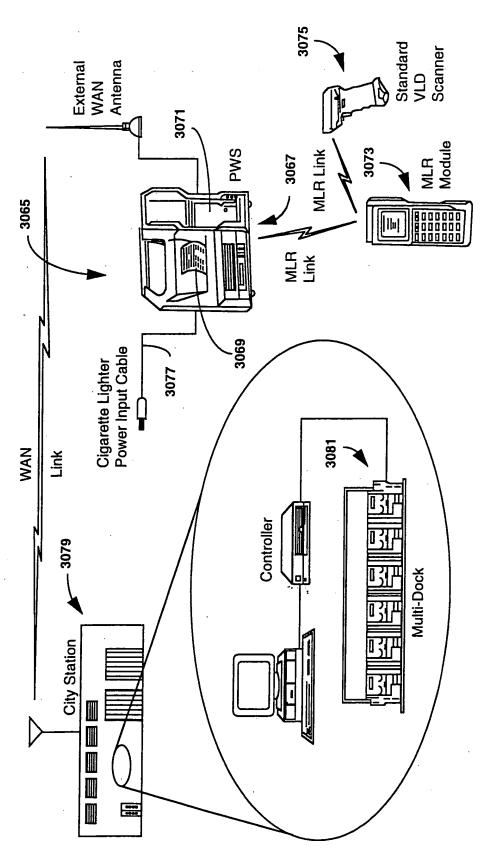


Fig. 29B



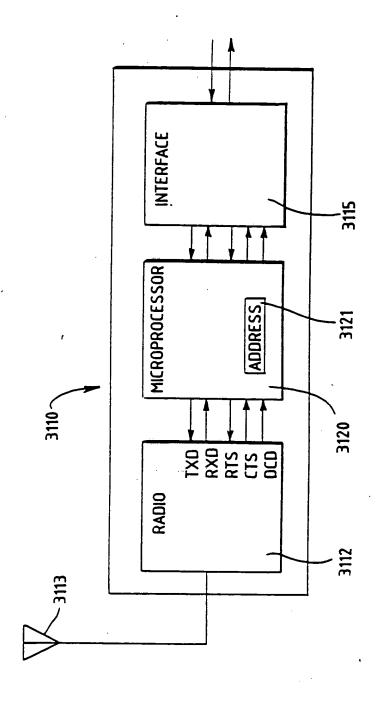


Fig. 30

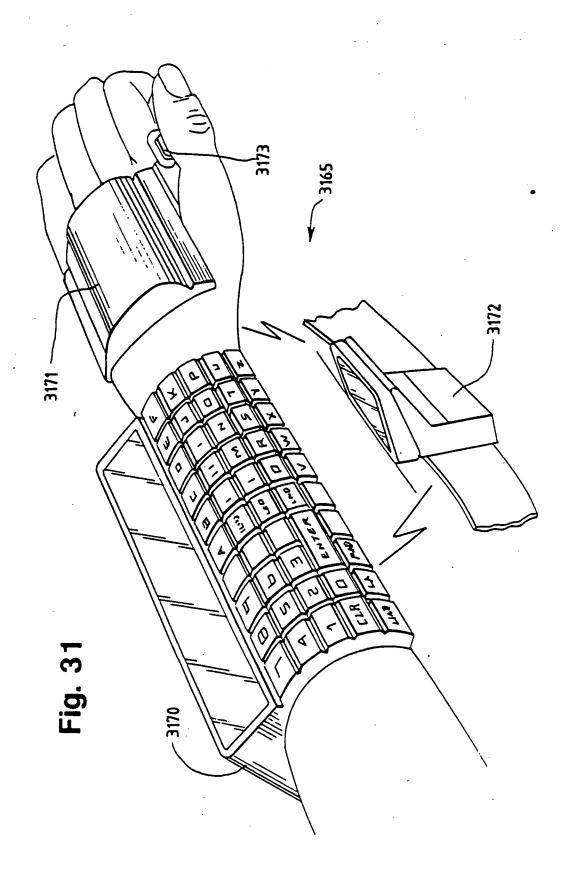
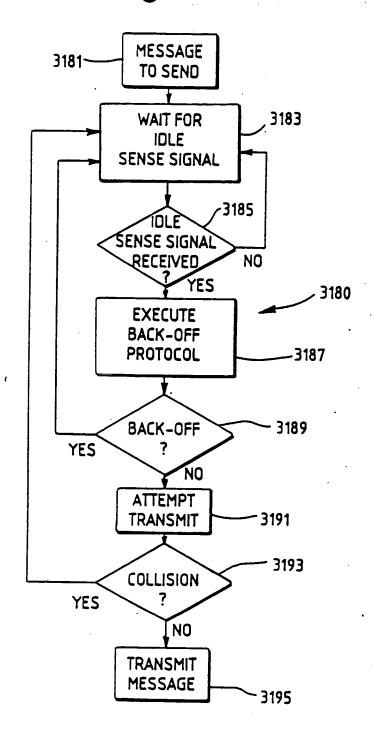


Fig. 32



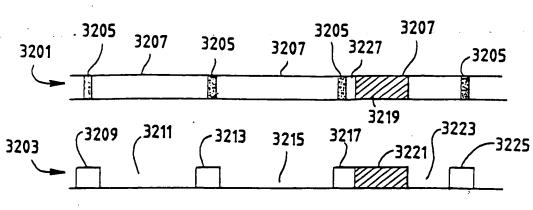
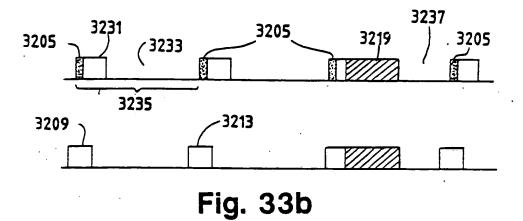
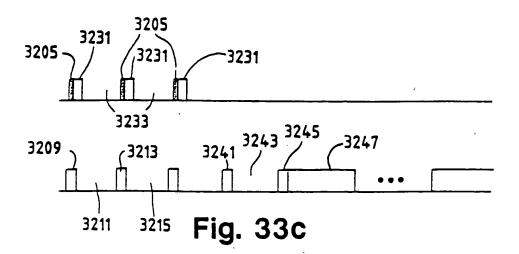


Fig. 33a





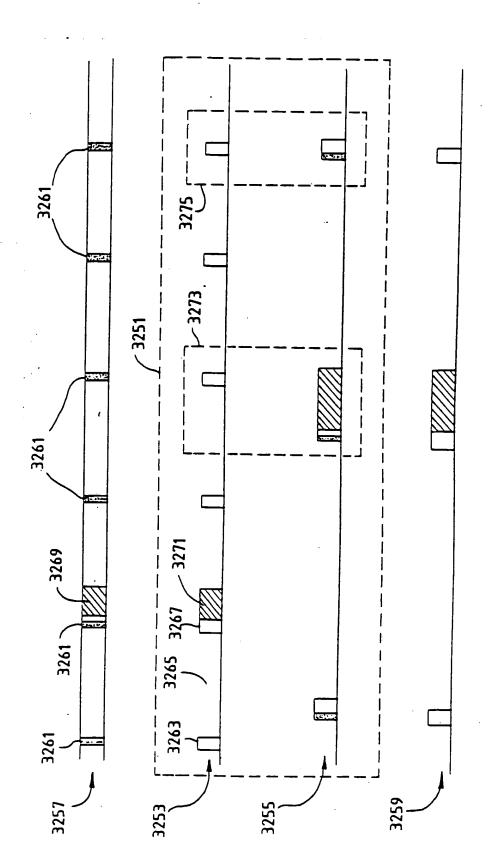


Fig. 34

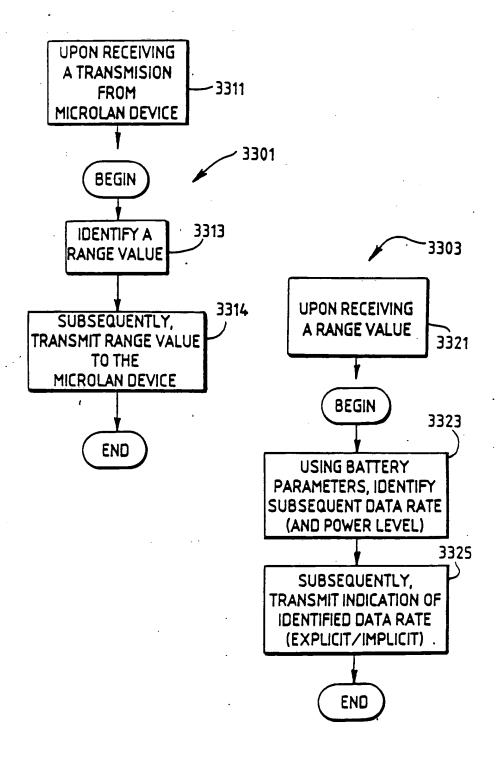
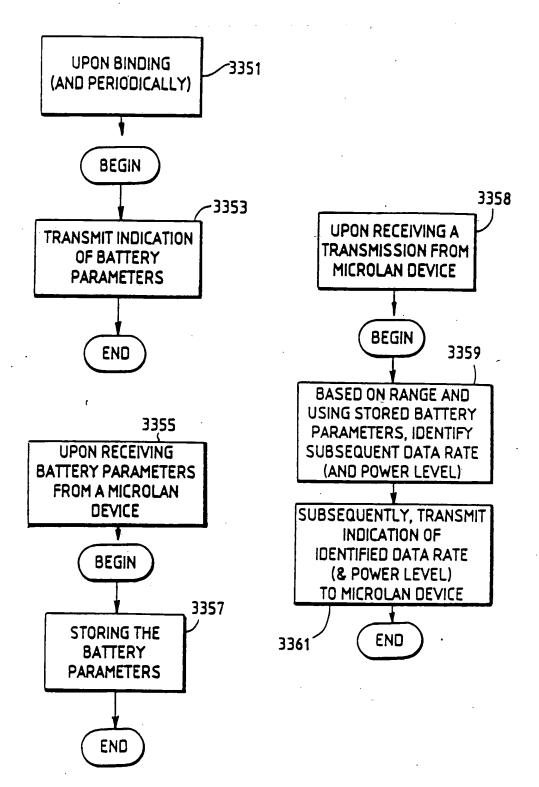


Fig. 35

Fig. 36



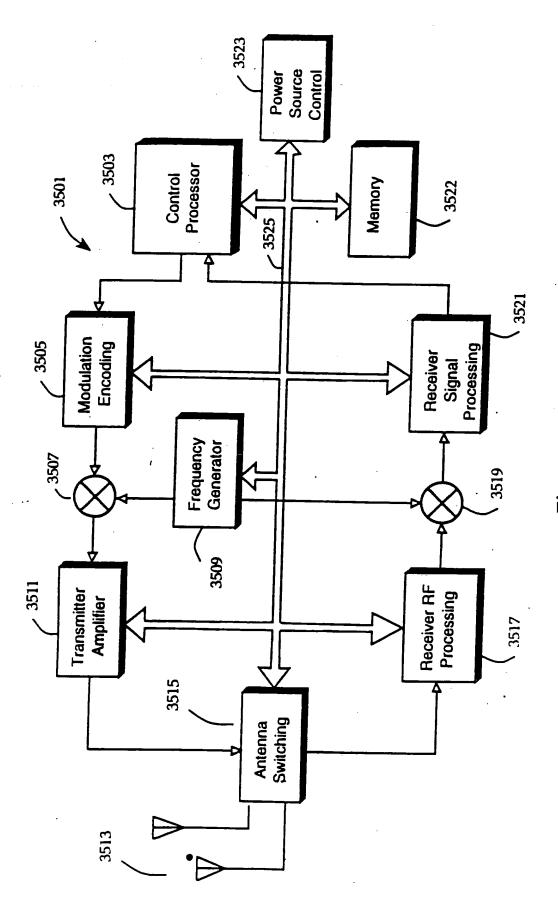


Figure 37

. 3509

Figure 38

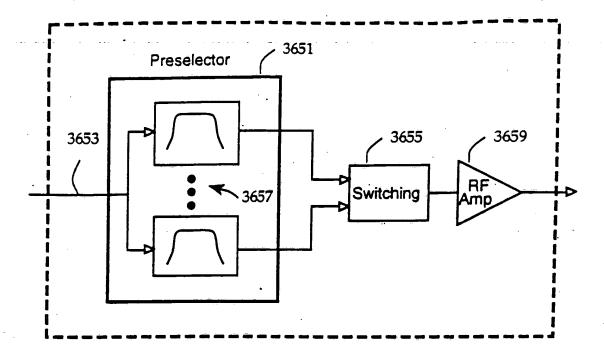


Figure 39

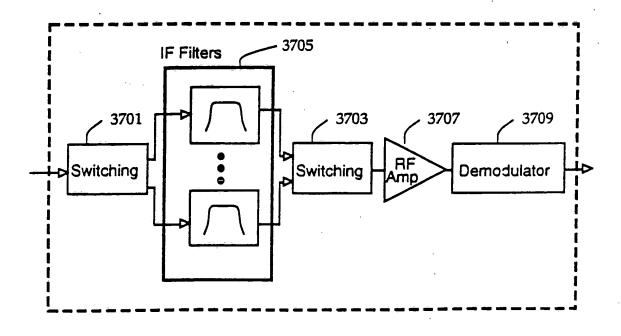


Figure 40

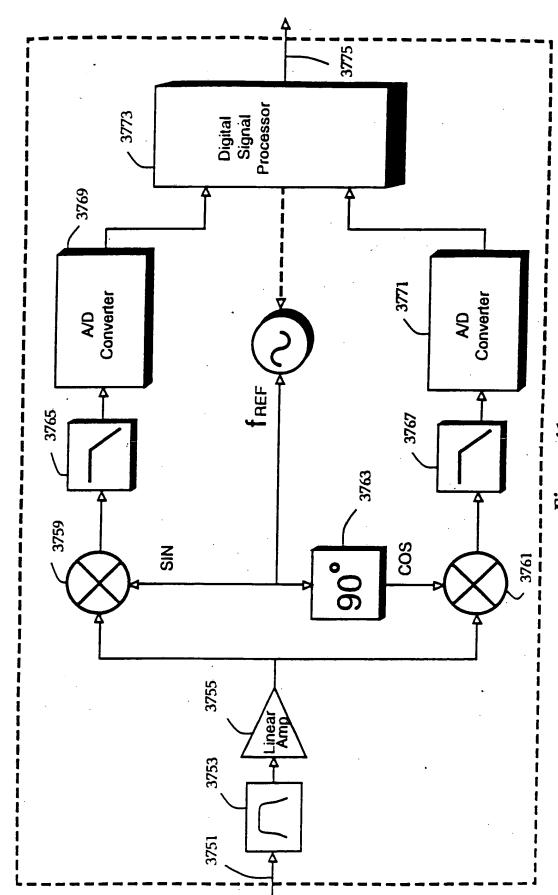


Figure 41

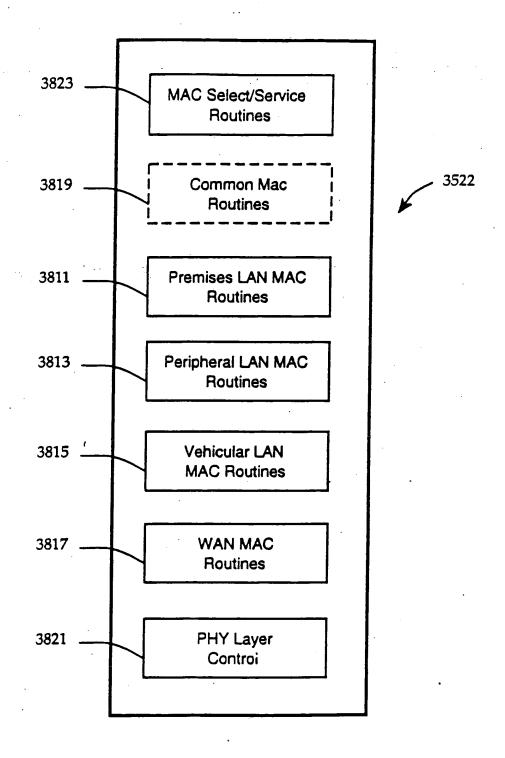
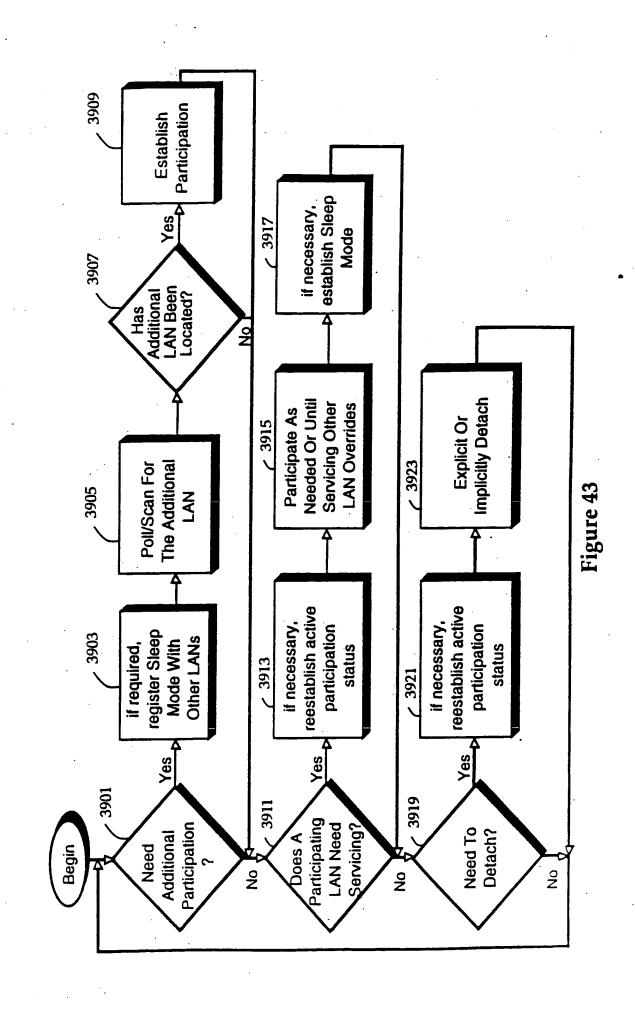


Figure 42



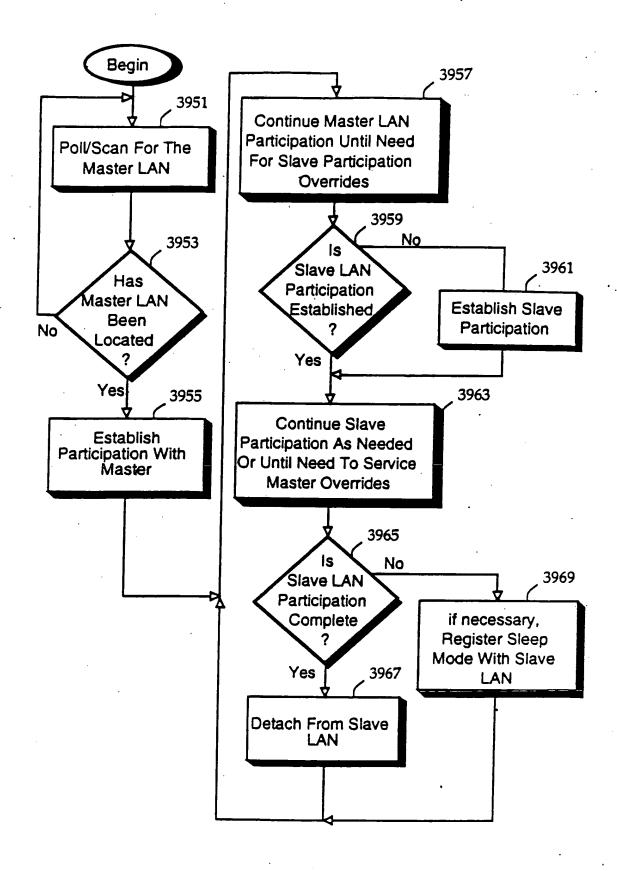
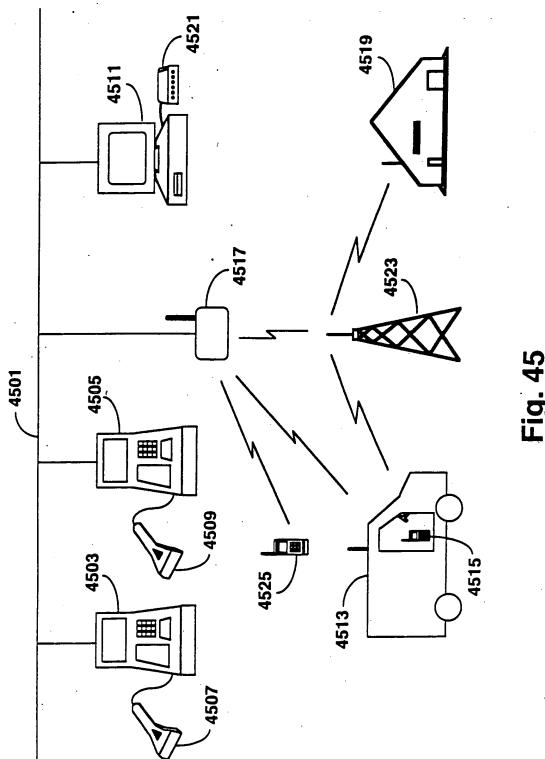


Figure 44



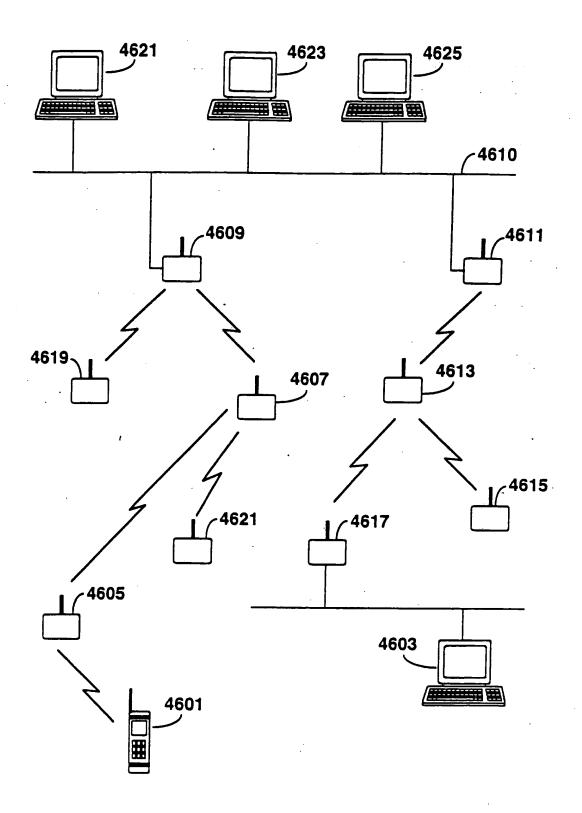


Fig. 46a

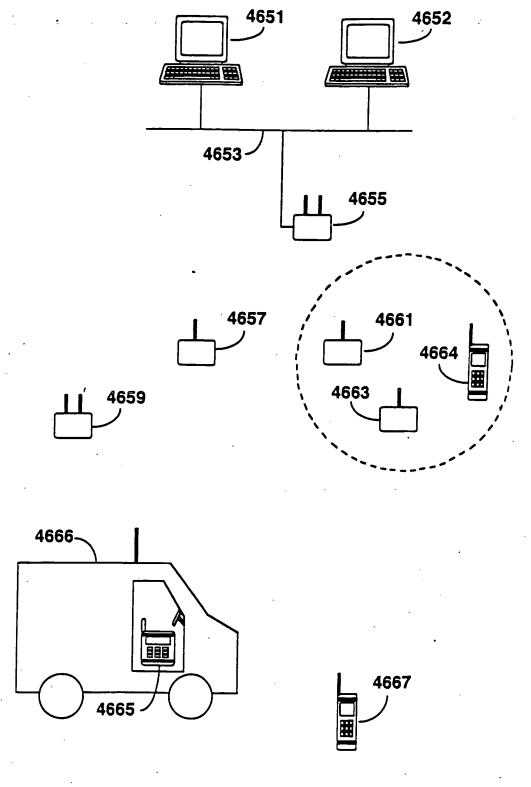


Fig. 46b

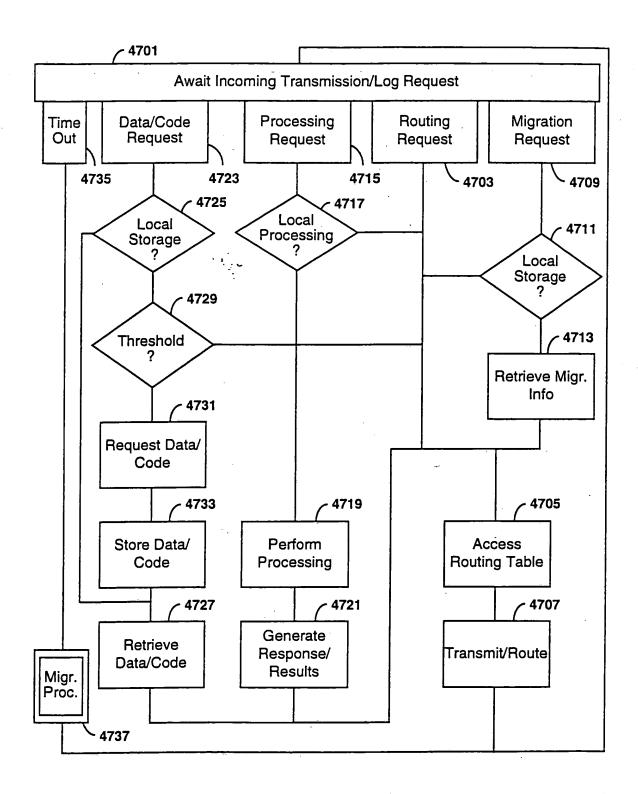


Fig. 47a

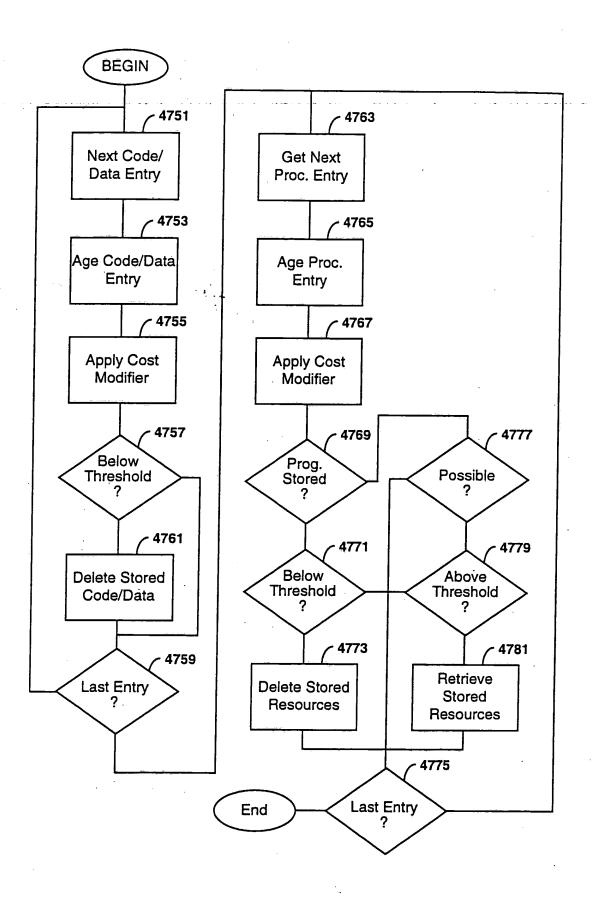


Fig. 47b

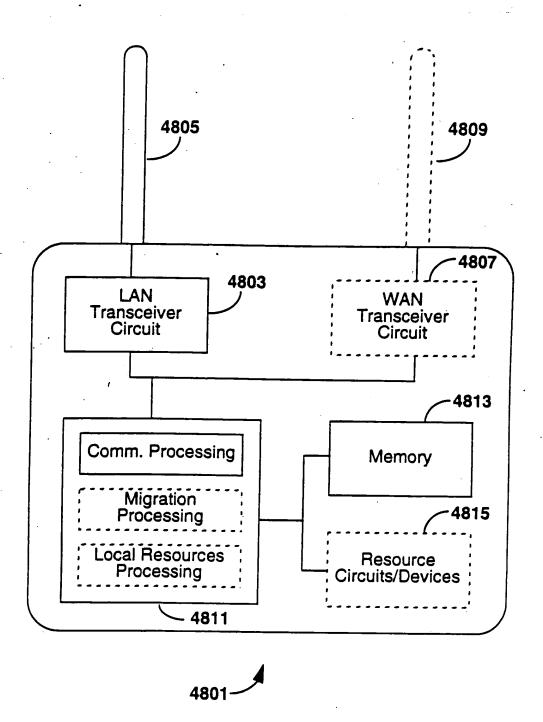


Fig. 48

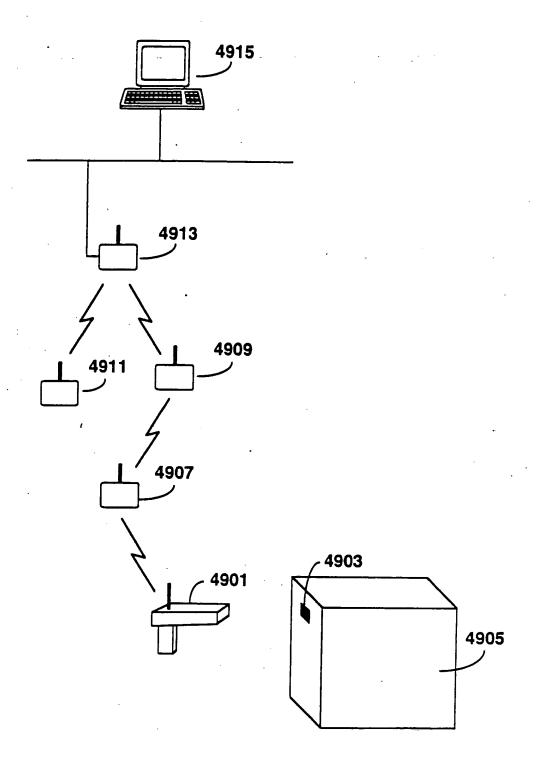


Fig. 49a

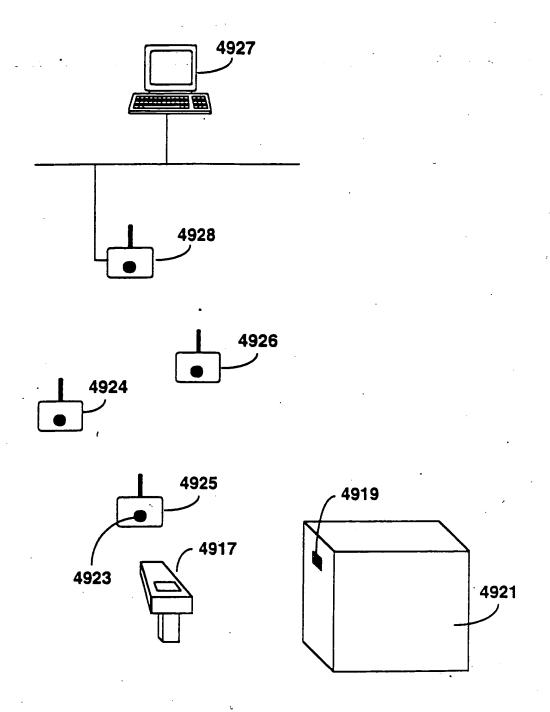


Fig. 49b

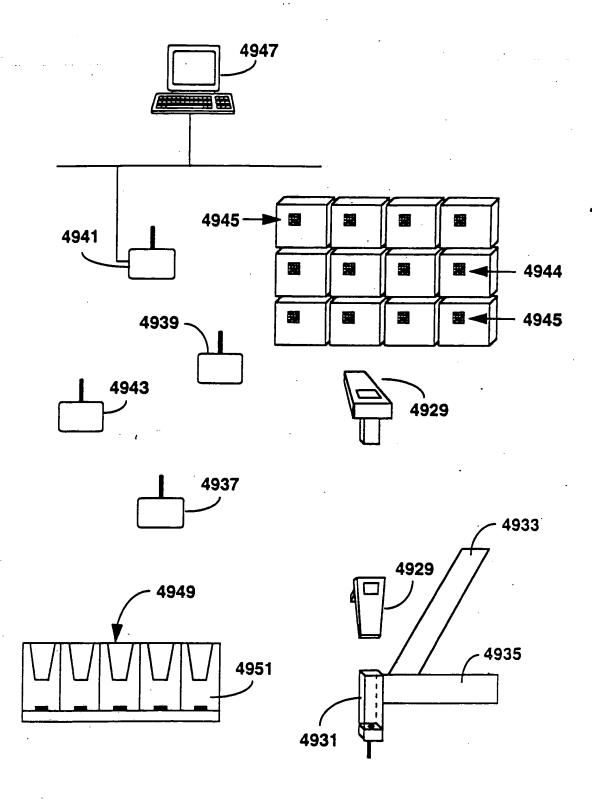


Fig. 49c

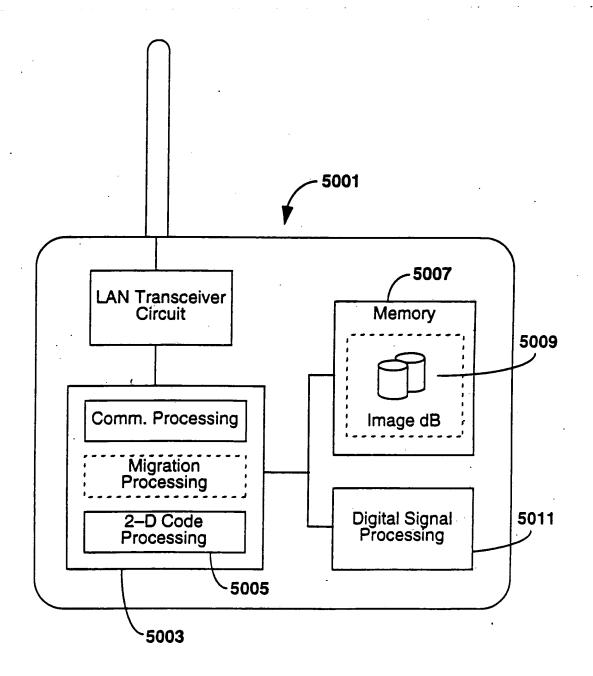


Fig. 50

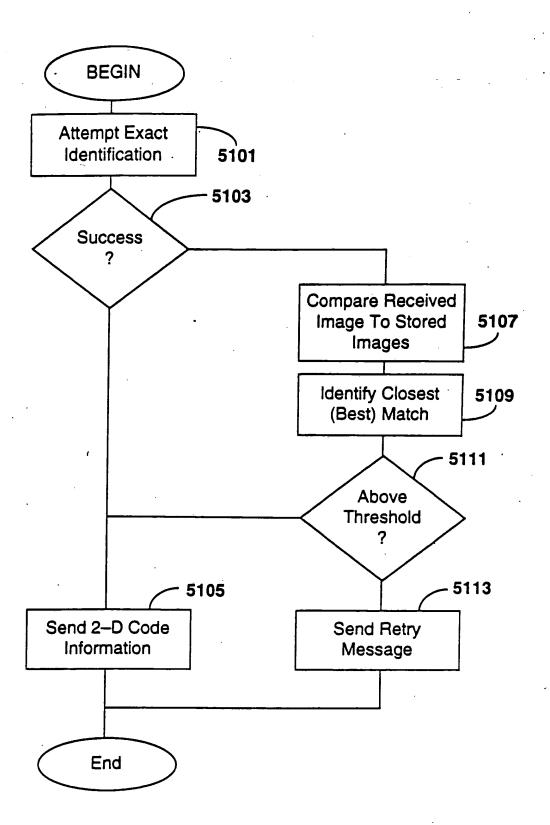


Fig. 51a

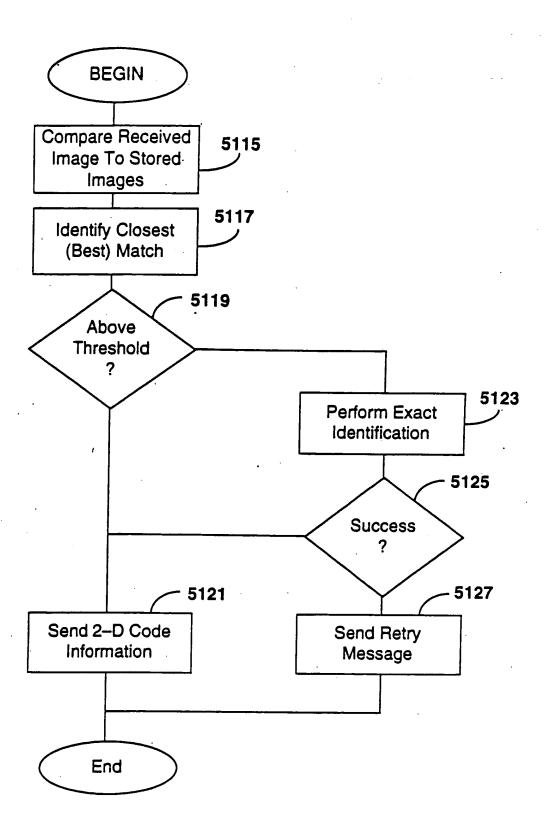


Fig. 51b

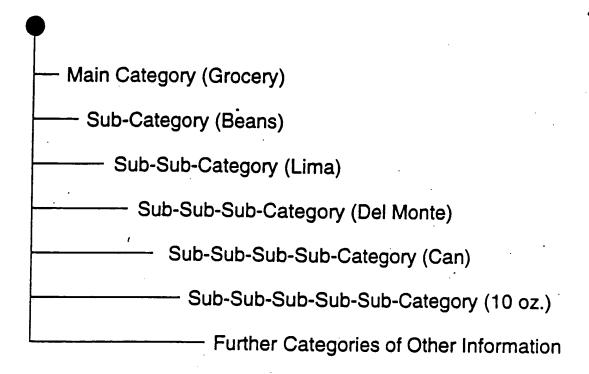


Fig. 52

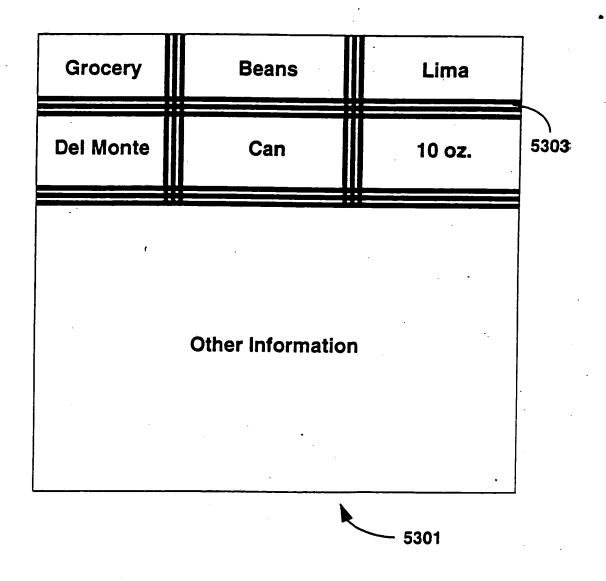


Fig. 53

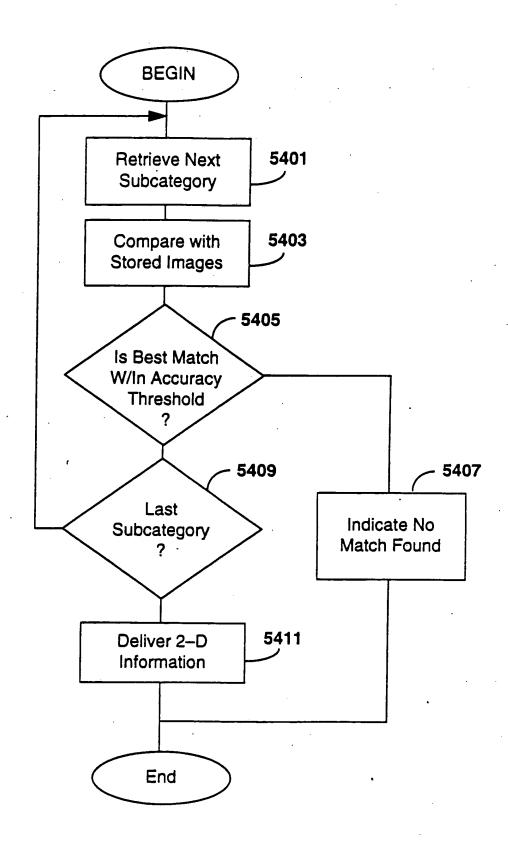


Fig. 54

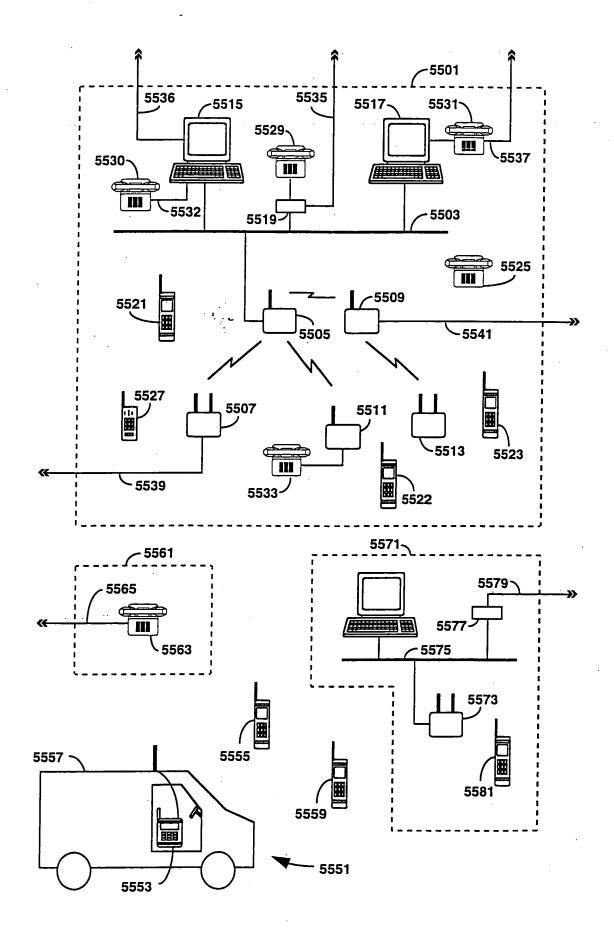
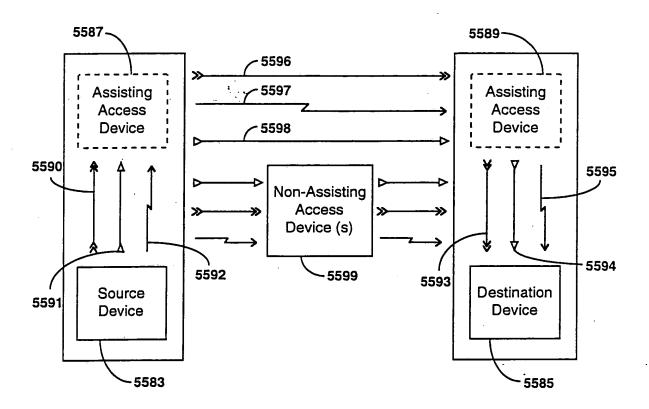


Fig. 55a



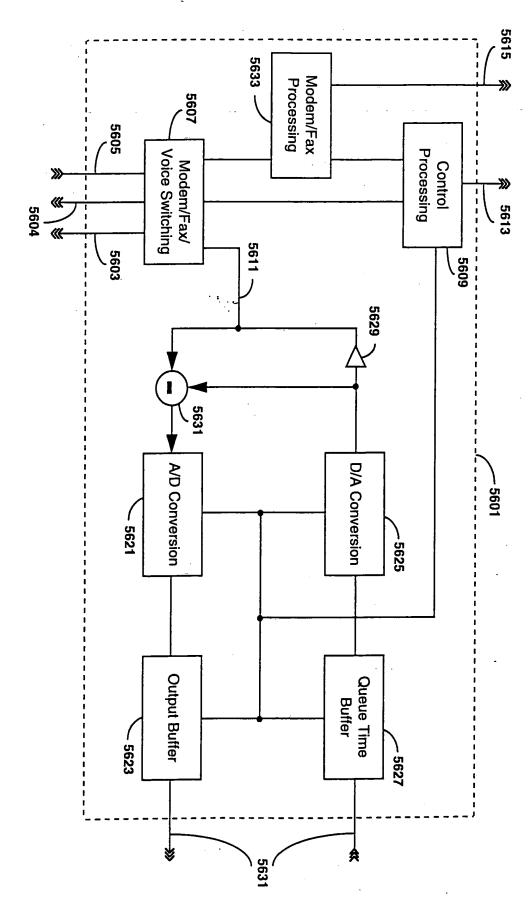
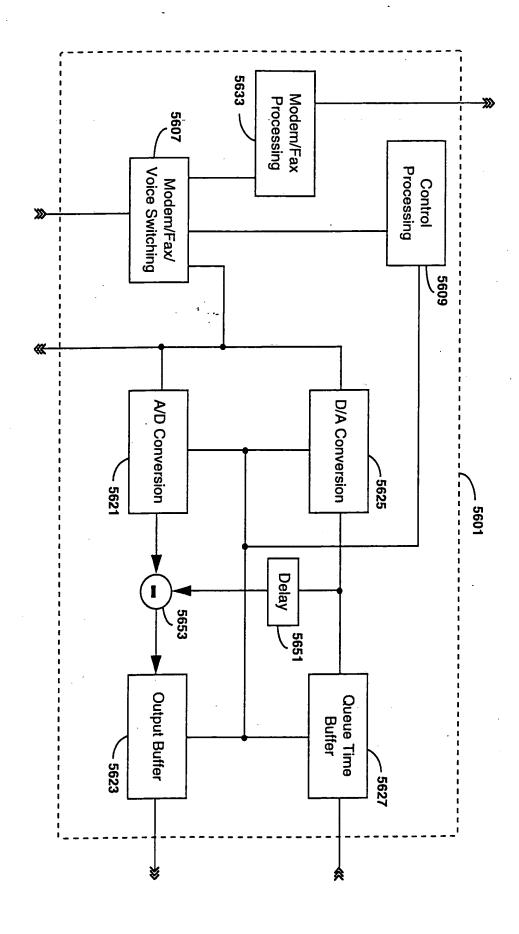
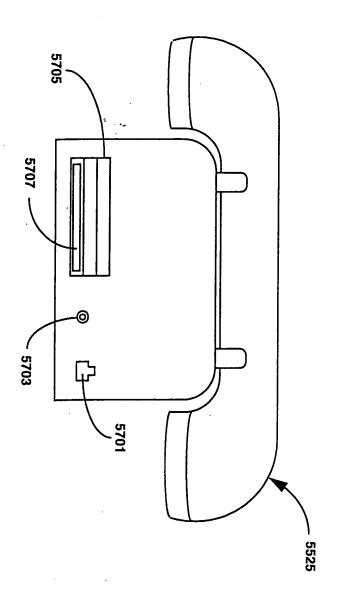


Fig. 56a



-ig. 56b



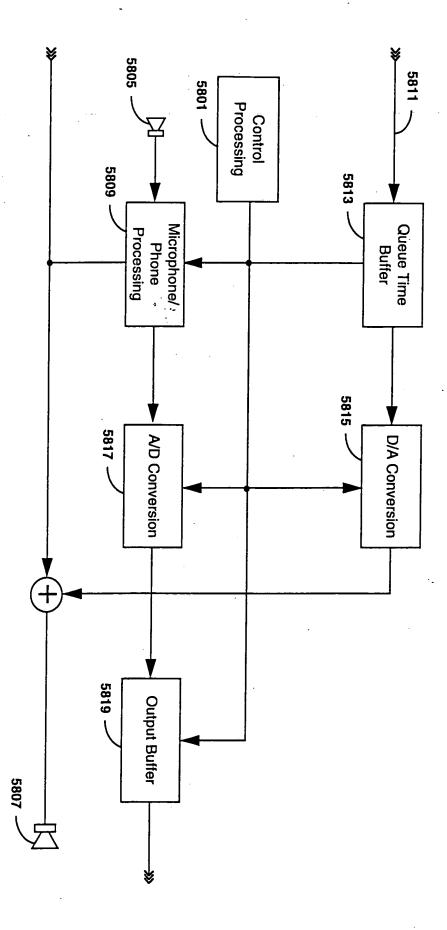


Fig. 58

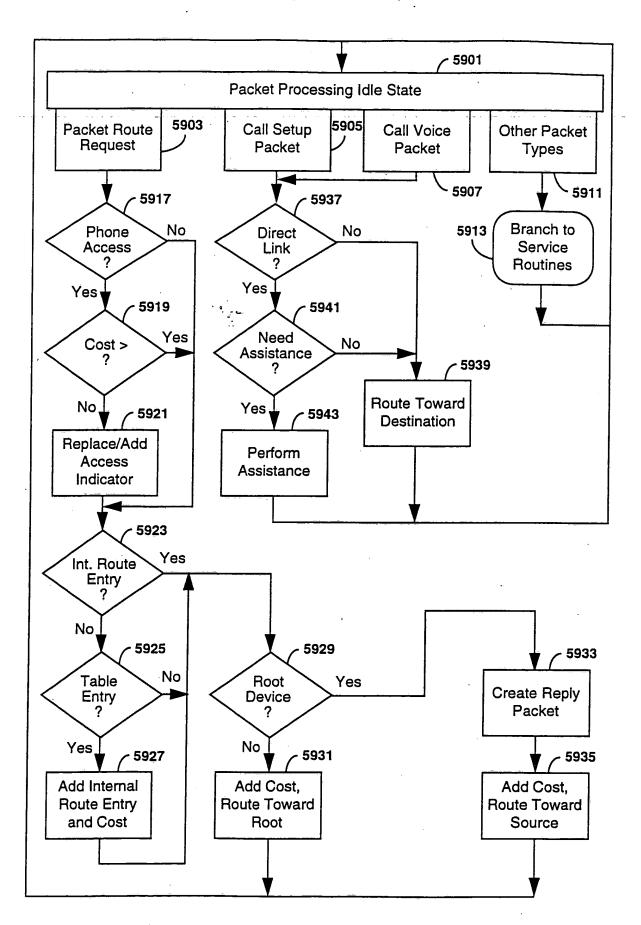


Fig. 59

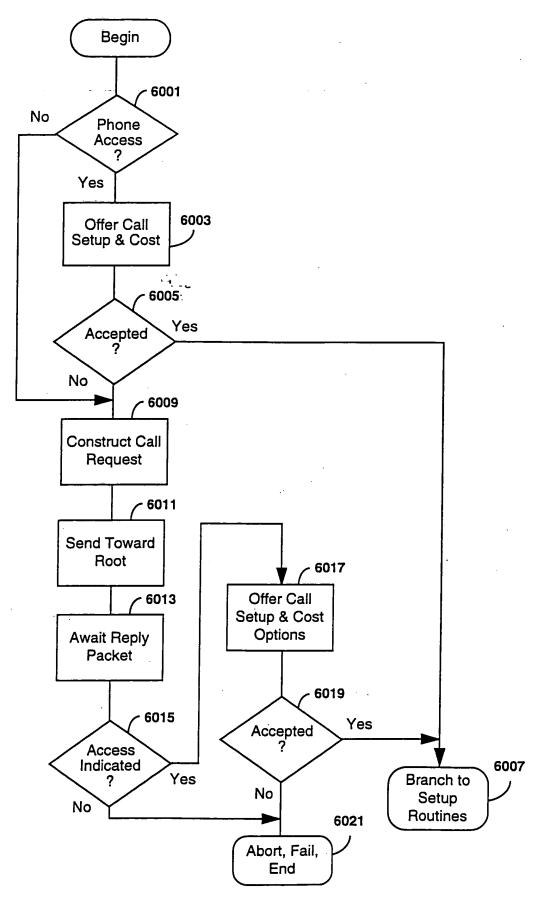


Fig. 60

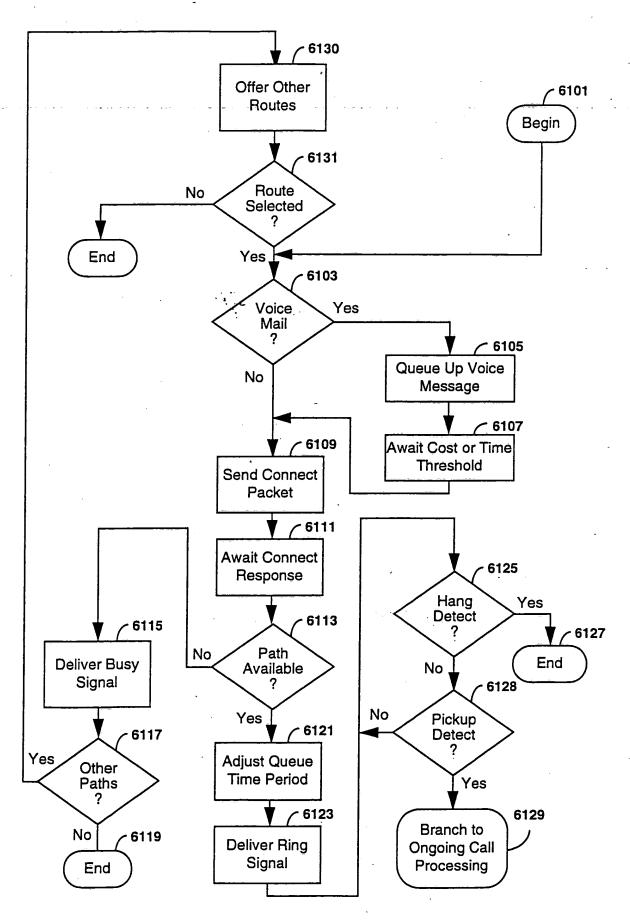


Fig. 61

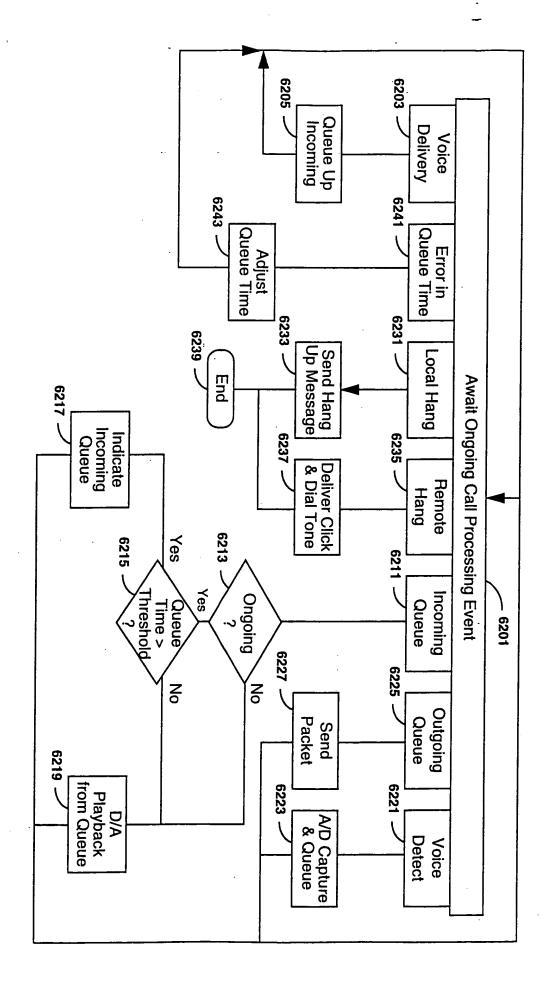


Fig. 62

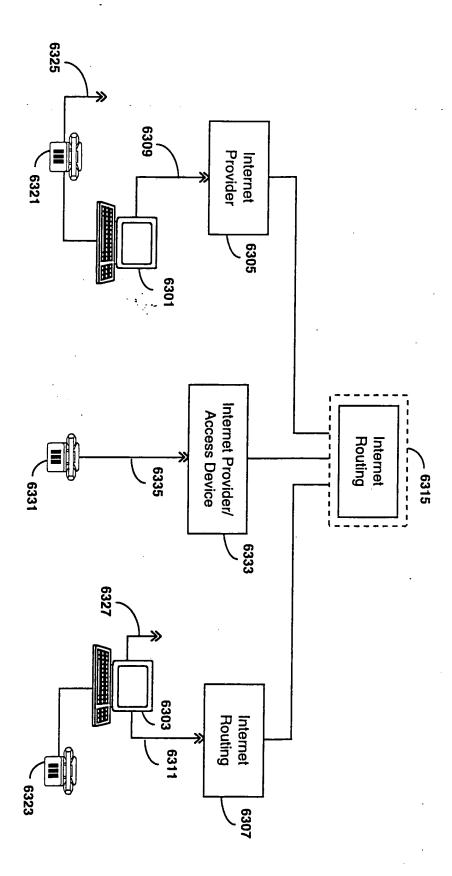


Fig. 63

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